Project:

Test Code for waveform and testbench simulation

Memory Assembly

address instruction Byte 1 Byte 2

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000000: LDI A, 5 01010000 00000101

000010: DEC A 11100010

000011: JNZ 000010 10000000 00000010

000101: INC A 11100001

000110: STM 001111, A 01000000 00001111

001000: STA 0, A 00100000

001001: ADD A, 0 11000000

001010: STM 010000, A, 01000000 00010000

001100: HALT 11110010